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2814

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PTO/SB/21(6-98)
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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/904,578
	Filing Date	July 12, 2001
	First Named Inventor	Richard C. Eder et al.
	Group Art Unit	2814
	Examiner Name	N. Ngo
Total Number of Pages in This Submission	Attorney Docket Number	IS9-017

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<input checked="" type="checkbox"/> Amendment / Response	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
<input type="checkbox"/> After Final	<input type="checkbox"/> Petition Routing Slip (PTO/SB/69) and Accompanying Petition	<input type="checkbox"/> Proprietary Information
<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Status Letter
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	<input checked="" type="checkbox"/> Additional Enclosure(s) (please identify below):
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<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	James D. Shaurette, Reg. No. 39,833 Wells, St. John, P.S.
Signature	
Date	7/15/02

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date: 07/15/2002	
Typed or printed name	Natalie King James J. Shaurette
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#7
Dimmick
8/5/02
Rmelt + A

Application Serial No. 09/904,578
Filing Date July 12, 2001
Inventor Richard C. Eden et al.
Assignee Isothermal Systems Research, Inc.
Group Art Unit 2812
Examiner N. Ngo
Attorney's Docket No. IS9-017
Title: Power Semiconductor Switching Devices, Power Converters, Integrated Circuit Assemblies, Integrated Circuitry, Power Current Switching Methods, Methods of Forming a Power Semiconductor Switching Device, Power Conversion Methods, Power Semiconductor Switching Device Packaging Methods, and Methods of Forming a Power Transistor

RESPONSE TO JUNE 13, 2002 OFFICE ACTION

To: Box Fee/Non-Fee Amendment
Assistant Commissioner for Patents
Washington, D.C. 20231

From: James D. Shaurette (Tel. 509-624-4276; Fax 509-838-3424)
Wells, St. John, P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

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Sir:
Responsive to the Office Action dated June 13, 2002, Applicants amend and remark as follows:

AMENDMENTS

In the Claims

Please replace the claims with the following clean version of the entire set of pending claims, in accordance with 37 C.F.R. § 1.121(c)(1)(I).

A marked up version showing amendments to any claims being changed is provided in one or more accompanying pages separate from this amendment in accordance with 37 C.F.R. § 1.121(c)(1)(ii).